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| Course title | |  | | --- | | **VLSI Design** | |
| Semester | Spring semester |
| Faculty / Department | Faculty of Computer Science |
| Professor | |  | | --- | | Professor Goran Slavković, PhD | |
| ECTS credits | 6 |
| Language of instruction | English |
| Level of study | Bachelor |
| Content | Design computer VLSI systems by using languages for hardware description: VHDL and Verilog. The principles of the design of RISC processor on the example of one RISC processor: the design phase, the decision making process.  Set of resolved tasks. Examples of resource design of processor and interconnections. Design, simulation and synthesis of a small, but functional processors in FPGA technology. |
| Learning outcomes | It is expected that students develop the ability to design VLSI computing systems independently. |
| Length | One semester |
| General information | Introducing students to the principles of VLSI systems. Presenting languages for hardware description. |
| Restrictions to mobile students and availability before the signature of the learning agreement | There is no any restrictions. |